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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

**Complete if Known**

Application Number	10/806,488
Filing Date	March 23, 2004
First Named Inventor	Cutts
Art Unit	<del>2183</del> 2133
Examiner Name	
Attorney Docket Number	Archivas - 0001

Sheet	1	of	2
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Attorney Docket Number	Archivas - 0001
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## U. S. PATENT DOCUMENTS

[illegible]

**FOREIGN PATENT DOCUMENTS**

[illegible]

**Examiner  
Signature**

Schults

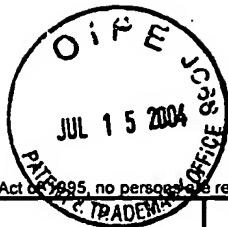
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PTO/SB/08B (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

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Sheet 2 of 2

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
SJS		GOOD et al., "A Robust Storage System Architecture," Technical Report: CS-95-10, Dept. of Computer Science, University of Waterloo, March 9, 1995	
SJS		BESTAVROS, "A VLSI Chip for the Real-time Information Dispersal and Retrieval for Security and Fault-Tolerance," Harvard University, September, 1989	
SJS		PLANK, "A Tutorial on Reed-Solomon Coding for Fault-Tolerance in RAID-like Systems," Technical Report UT-CS-96-332, Dept. of Comp. Science, Univ. of Tennessee, July 19, 1996	

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11/20/06

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